



501.41261X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): T. ARAI et al  
Application No.: 10/085,063  
Filed: March 1, 2002  
Allowed: October 7, 2004  
For: FABRICATION METHOD OF SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE  
Group: 1765  
Examiner: B. X. Tran

**SUBMISSION UNDER 37 CFR § 1.114**  
**(AMENDMENT)**

January 6, 2005

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Concurrently with the filing of a Request For Continued Examination (RCE)

Transmittal, please amend the above-identified application, prior to further  
examination thereof, as listed in the following, and as set forth on the following  
pages:

Amendments to the Claims; and

Remarks are included following the amendments.